

Figure 4

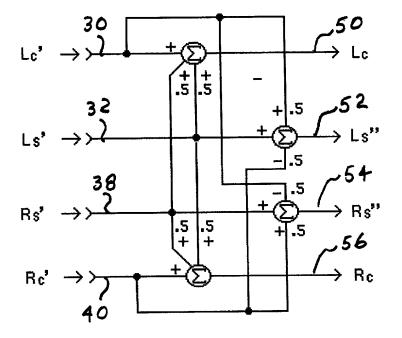


Figure 5

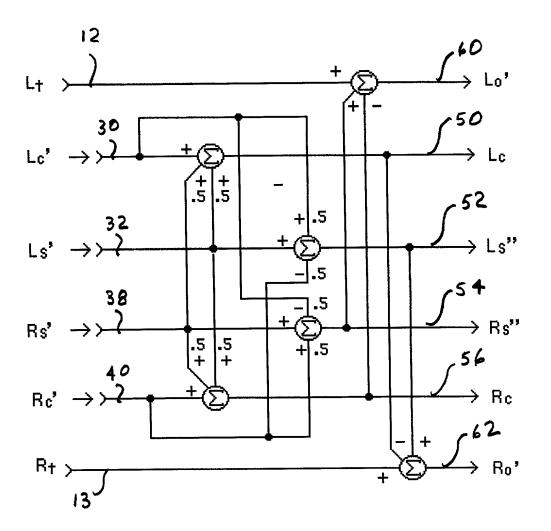


Figure 6

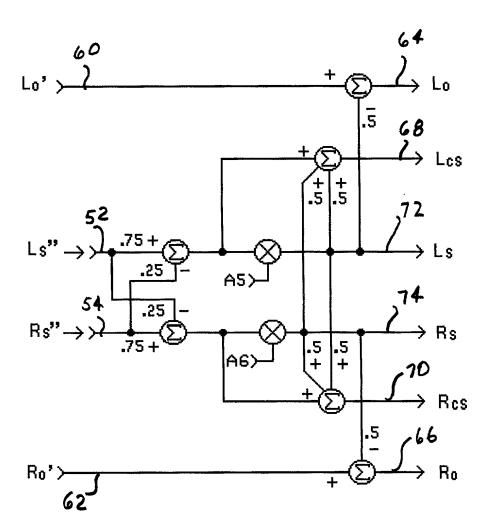


Figure 7

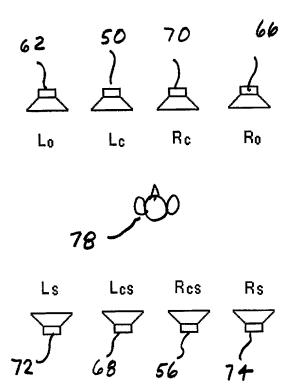


Figure 8

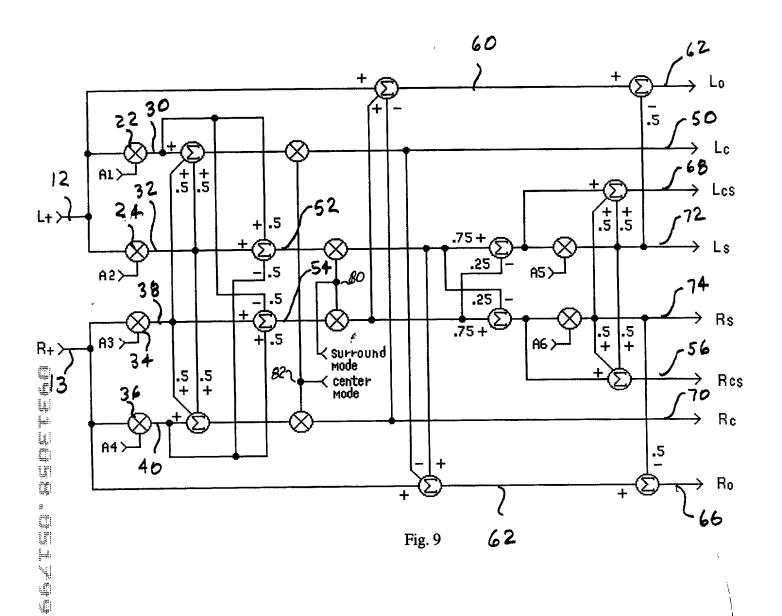
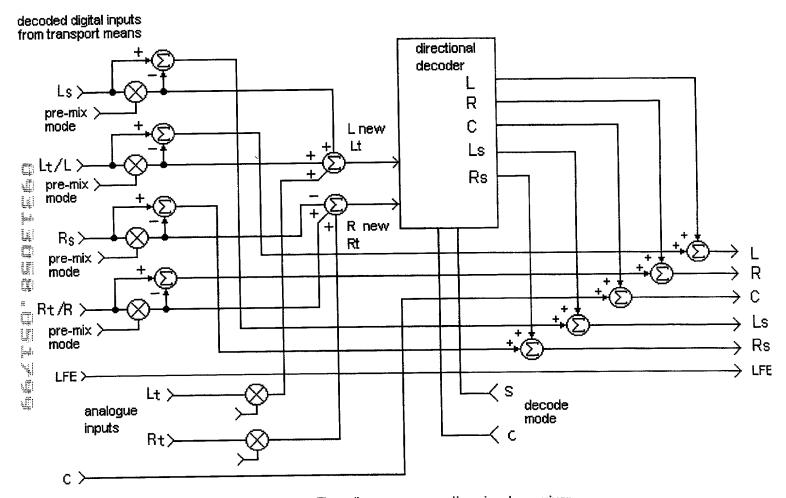


FIG. 10



Flow diagram representing signal pre-mixer in combination with an "n" channel directional decoder where "n" ≤ 5